

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	09488942
Filing Date	2000-01-21
First Named Inventor	Paul W. SHERER
Art Unit	2152
Examiner Name	Sean M. REILLY
Attorney Docket Number	1112.US.CIP

**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	2	4500990		1985-02-19		
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	8	4969164		1990-11-06		

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**U.S.PATENT APPLICATION PUBLICATIONS**

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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	NATIONAL SEMICONDUCTOR, NS 16550A Universal Asynchronous Receiver/Transmitter with FIFOs, 1988.	<input type="checkbox"/>
	2	KANAKIA, HEMART and DAVID CHERITON, "The VMP Network Adapter Board (NAB): High-Performance Network Communication for Multiprocessors", ACM, 1988.	<input type="checkbox"/>
	3	FUJITSU, LTD, Data sheet for "FUJITSU MB86950 Etherstar Ethernet Controller", December 1989.	<input type="checkbox"/>
	4	TEXAS INSTRUMENTS, TL16C550A Asynchronous Communications Element, March 1996.	<input type="checkbox"/>

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5	FRAZIER, GREGORY L and YUVAL TARIAR, "The Design and Implementation of a Multi-Queue Buffer for VLSI Communication Switches", IEEE, 1989.	<input type="checkbox"/>
6	INTEL, CORP, Datasheet for 82596CA High-Performance 32-Bit Local Area Network Coprocessor", November 1989.	<input type="checkbox"/>
7	NGAI, JOHN L and CHARLES L. SEITZ, "A Framework for Adaptive Routing in Multicomputer Networks", ACM, 1989.	<input type="checkbox"/>
8	SCHRODER, MICHEAL D et al, "Autonet: A High-Speed, Self-Configuring Local Area Network Using Point-to-Point Links", PRINCIPALS OF COMPUTING SYSTEMS, April 21, 1990.	<input type="checkbox"/>
9	GANSSE, JACK G., "Speed Kills", EMBEDDED SYSTEMS PROGRAMMING, August 1990.	<input type="checkbox"/>
10	NATIONAL SEMICONDUCTOR, Data sheet for DP8390C/NS32490C chip, "Data Communications Local Area Network UARTS", 1990.	<input type="checkbox"/>
11	NATIONAL SEMICONDUCTOR, Data sheet for "DP 83265 BSI Device (FDDI System Interface)", February 1991.	<input type="checkbox"/>
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13	ADVANCED MICRO DEVICES, Data sheet for "The SUPERNET 2 Family for FDDI", October 1991.	<input type="checkbox"/>
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15	OWICKI, SUSAN and ANNA KARLIN, "Factors in the Performance of the ANI Computer Network", ACM SIGMETRICS and PERFORMANCE, June 1992.	<input type="checkbox"/>

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16	MOTOROLA, "FDDI System Interface Users Manual, MC68839", 1994.	<input type="checkbox"/>
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**EXAMINER SIGNATURE**

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

**OR**

☐ That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(2).


☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

**SIGNATURE**

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature		Date (YYYY-MM-DD)	2007-6-18
Name/Print	Richard A. BAKER, Jr.	Registration Number	48,124

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**